

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of :

Matthew Russell

Serial No.: 10/648,054

Filed: 26-August-2003

For: Multi-Layer Staggered Power Bus  
Layout Design



Group Art Unit: 2826

Examiner: Andujar, Leonardo

Atty Docket: 03-0154

I hereby certify that this correspondence is being deposited with the U.S.  
Postal Service as First Class Mail in an envelope addressed to:  
Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450,  
on the date below:

Connie Del Castillo

5/6/06 *Connie Del Castillo*  
Date Signature

SUBMISSION OF FORMAL DRAWINGS PURSUANT TO 37 C.F.R. §1.85

Official Draftsman

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Applicant hereby substitutes the enclosed formal drawings for those presently in the above referenced application.

LSI Logic Corporation  
1621 Barber Lane, MS D-106  
Milipitas, CA 95035  
(408) 433-8000

Date:

4 MAY 06

Respectfully submitted,

Timothy Croll

Reg. No.: 36,771